



**8.4 inch TFT LCD  
without Touch Panel  
SPECIFICATION**

**MODEL NAME: LMTFA084BS11**

**Date: 2012 / 07 / 18**

<b>Customer Signature</b>		
<b>Customer</b>		
<b>Approved Date</b>	<b>Approved By</b>	<b>Reviewed By</b>

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## Record of Revision

Version and Date	Page	Old description	New Description
1.0 2012/07/18		First Edition	



## 1. Operating Precautions

- 1) Since front polarizer is easily damaged, please be cautious not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
- 5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
- 6) To avoid ESD (Electro Static Discharge) damage, be sure to ground yourself before handling TFT-LCD Module.
- 7) Do not open nor modify the module assembly.
- 8) Do not press the reflector sheet at the back of the module to any direction.
- 9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the LED Reflector edge. Instead, press at the far ends of the LED Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) After installation of the TFT Module into an enclosure (Notebook PC Bezel, for example), do not twist nor bend the TFT Module even momentarily. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 13) Severe temperature condition may result in different luminance, response time.
- 14) Continuous operating TFT-LCD Module under high temperature environment may accelerate LED light bar exhaustion and reduce luminance dramatically.
- 15) The data on this specification sheet is applicable when LCD module is placed in landscape position.
- 16) Continuous displaying fixed pattern may induce image sticking. It is recommended to use screen saver or shuffle content periodically if fixed pattern is displayed on the screen.



## 2. General Description

This specification applies to the 8.4 inch color TFT LCD module LMTFA084BS11.

LMTFA084BS11 designed with wide viewing angle; wide operating temperature and long life LEDs backlight is well suited to be the display units for Industrial Applications.

LED driving board for backlight unit is included in this panel and the structure of the LED units is replaceable.

LMTFA084BS11 is built in timing controller and LVDS interface.

The screen format is intended to support the SVGA (800(H) x 600(V)) screen and 16.2M (RGB 8-bits) or 262k colors (RGB 6-bits).

LMTFA084BS11 is a RoHS product.

### 2.1 Display Characteristics

The following items are characteristics summary on the table under 25°C condition:

Items	Unit	Specifications
Screen Diagonal	[inch]	8.4 ( 213.4mm )
Active Area	[mm]	170.4(H) x 127.8(V)
Pixels H x V		800x3(RGB) x 600
Pixel Pitch	[mm]	0.213x0.213
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN, Normally White
Nominal Input Voltage VDD	[Volt]	3.3 (typ)
Typical Power Consumption	[Watt]	4.74 (typ)
Weight	[Grams]	250 (typ)
Physical Size	[mm]	203.0(W) x 142.5(H) x 8.0(D) (typ.)
Electrical Interface		1 channel LVDS
Surface Treatment		Anti-glare, Hardness 3H
Support Color		262K(6-bit) / 16.2M(8-bit)
Temperature Range Operating Storage (Non-Operating)	°C °C	-30 to +85 (panel surface temperature) -30 to +85
RoHS Compliance		RoHS Compliance



## 2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25 °C (Room Temperature):

Item	Unit	Conditions	Min.	Typ.	Max.	Note
White Luminance	[cd/m <sup>2</sup> ]	I <sub>F</sub> = 200mA (center point)	900	1000	-	1
Uniformity	%	9 Points	70	75	-	1, 2, 3
Contrast Ratio			400	600	-	4
Response Time	[msec]	Rising	-	20	30	5
	[msec]	Falling	-	10	20	
	[msec]	Raising + Falling	-	30	50	
Viewing Angle	[degree]	Horizontal (Right) CR ≥ 10 (Left)	70	80	-	6
	[degree]		70	80	-	
	[degree]	Vertical (Upper) CR ≥ 10 (Lower)	65	80	-	
	[degree]		50	60	-	
Color / Chromaticity Coordinates (CIE 1931)		Red x	0.559	0.609	0.659	1
		Red y	0.283	0.333	0.383	
		Green x	0.315	0.365	0.415	
		Green y	0.520	0.570	0.620	
		Blue x	0.101	0.151	0.201	
		Blue y	0.056	0.106	0.156	
		White x	0.26	0.31	0.36	
		White y	0.28	0.33	0.38	
Color Gamut	%			45	-	1

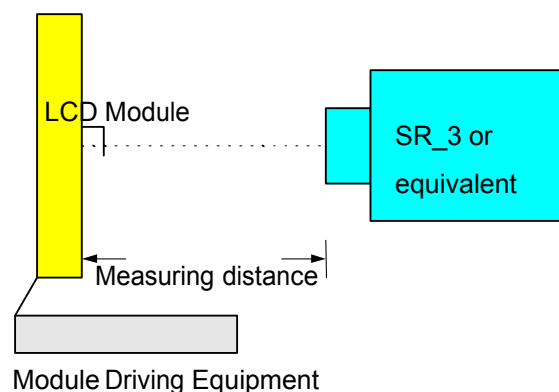
Note 1: Measurement method

Equipment : Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter (SR\_3 or equivalent)

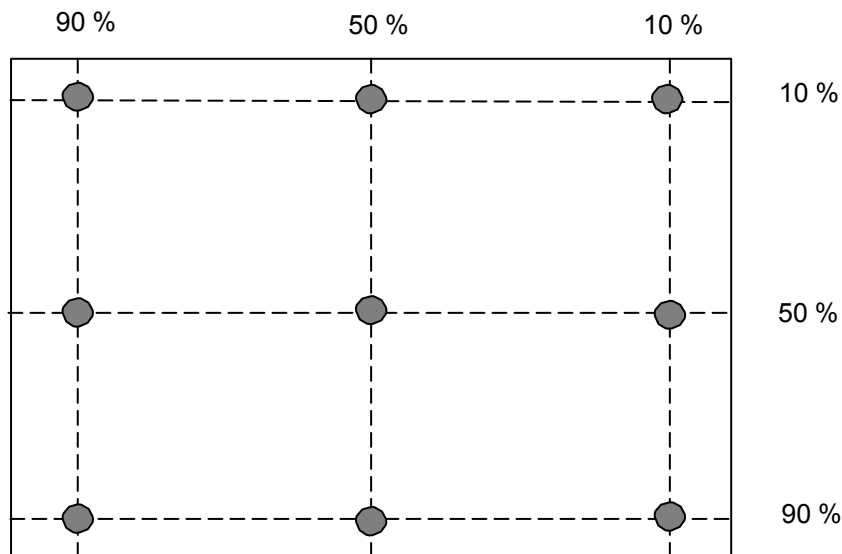
Aperture 1" with 50cm viewing distance

Test Point Center

Environment < 1 lux



Note 2: Definition of 9 points position (Display active area : 170.4(H) x 127.8(V))



Note 3: The luminance uniformity of 9 points is defined by dividing the minimum luminance value by the maximum test point luminance

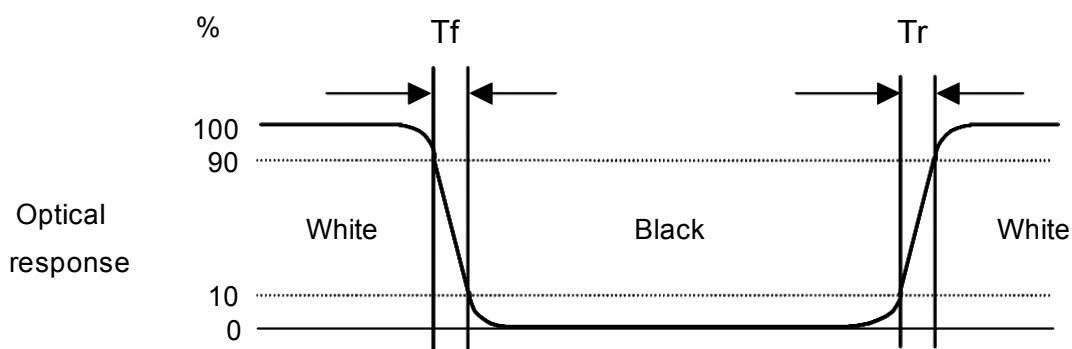
$$\delta_{W9} = \frac{\text{Minimum Brightness of nine points}}{\text{Maximum Brightness of nine points}}$$

Note 4 : Definition of contrast ratio (CR):

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "White" state}}{\text{Brightness on the "Black" state}}$$

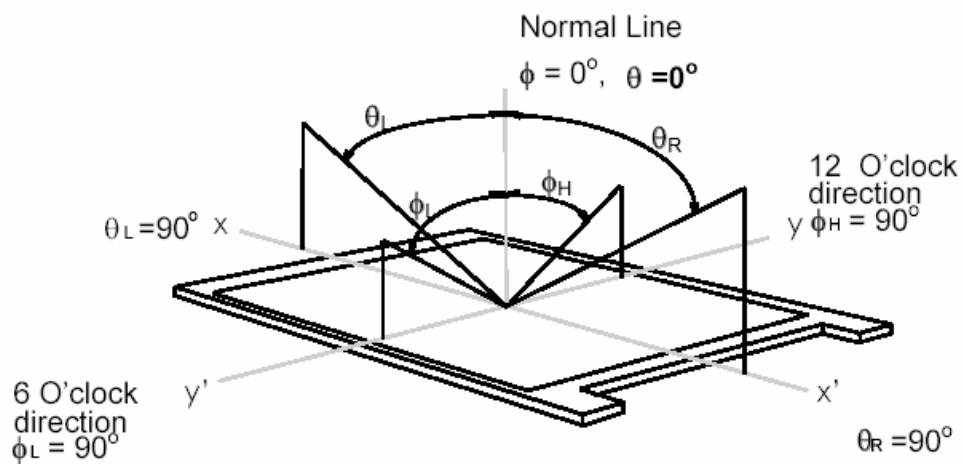
Note 5: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "White" to "Black" (falling time) and from "Black" to "White" (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.



Note 6: Definition of viewing angle

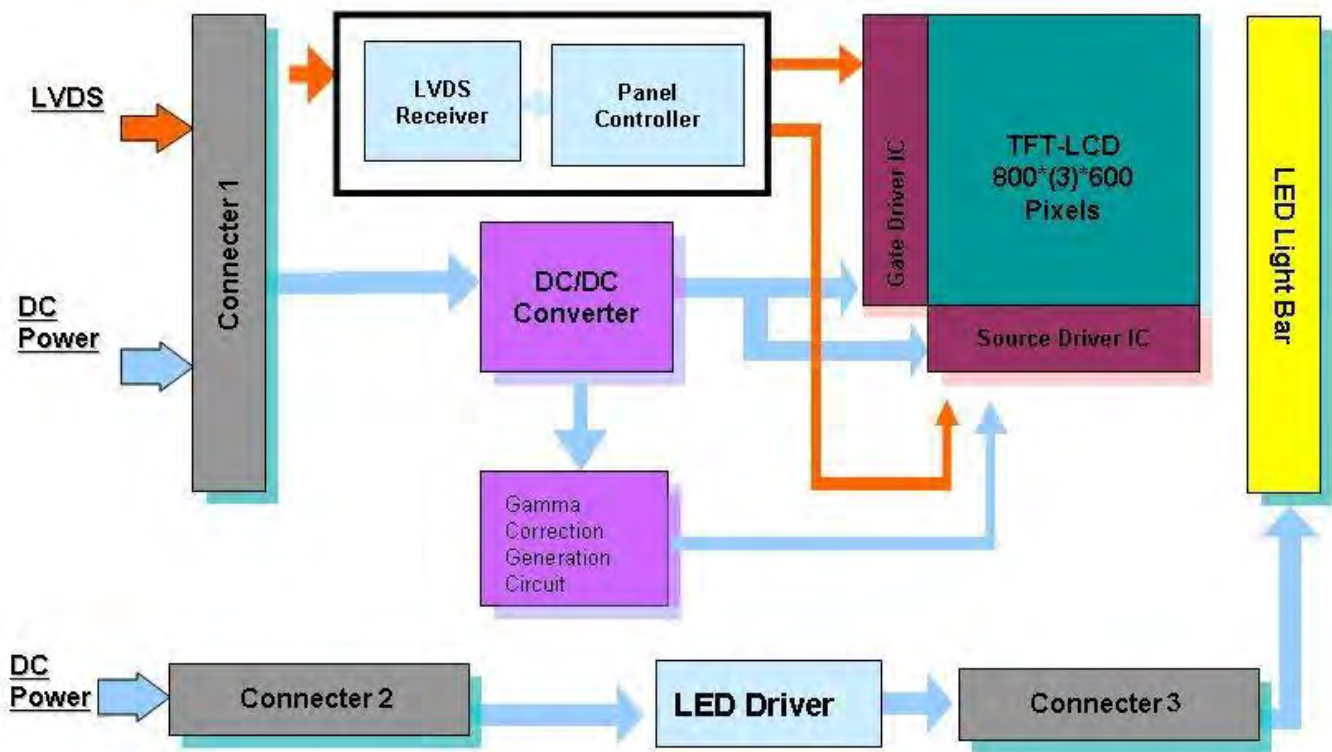
Viewing angle is the measurement of contrast ratio  $\geq 10$ , at the screen center, over a  $180^\circ$  horizontal and  $180^\circ$  vertical range (off-normal viewing angles). The  $180^\circ$  viewing angle range is broken down as below:  $90^\circ$  ( $\theta$ ) horizontal left and right, and  $90^\circ$  ( $\phi$ ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.





### 3. Functional Block Diagram

The following diagram shows the functional block of the 8.4 inch color TFT/LCD module:



## 4. Absolute Maximum Ratings

### 4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive Voltage	VDD	-0.3	+3.6	[Volt]	

### 4.2 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit
Operating Temperature	TOP	-30	+85	[°C]
Operation Humidity	HOP	5	90	[%RH]
Storage Temperature	TST	-30	+85	[°C]
Storage Humidity	HST	5	90	[%RH]

Note: Maximum Wet-Bulb should be 39 °C and no condensation.



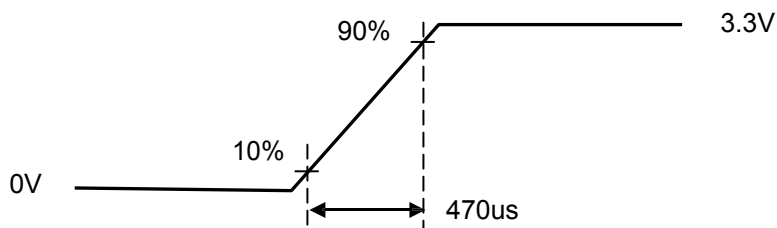
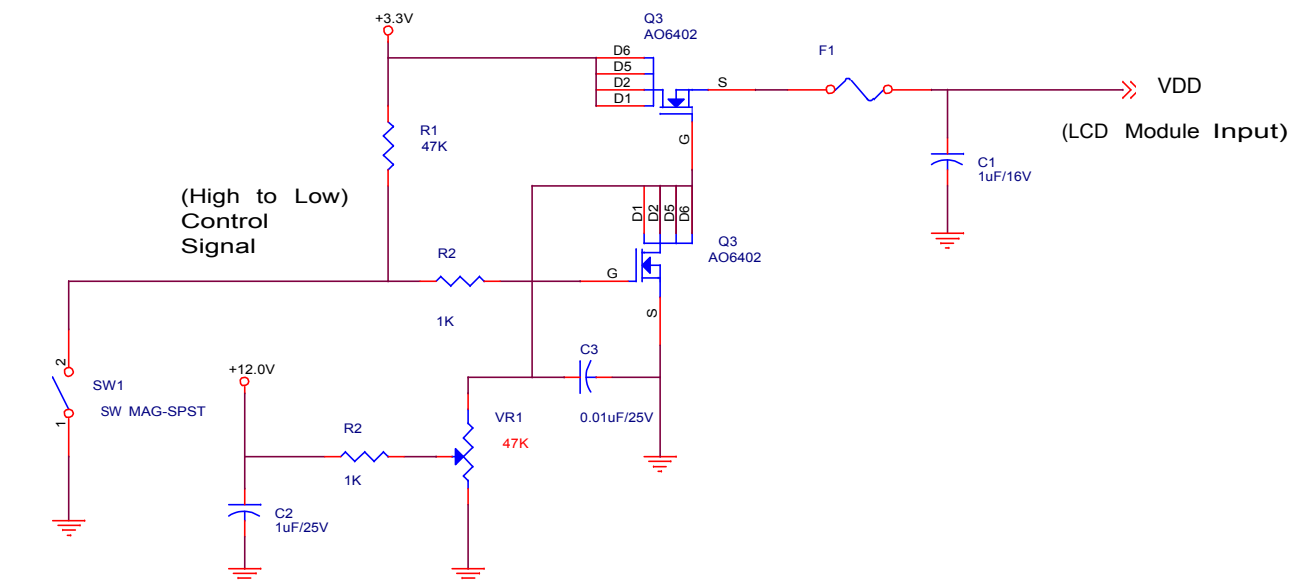
## 5. Electrical Characteristics

### 5.1 TFT LCD Module

#### 5.1.1 Power Specification

Symbol	Parameter	Min	Typ	Max	Units	Remark
VDD	Logic/LCD Drive Voltage	3.0	3.3	3.6	[Volt]	±10%
I <sub>VDD</sub>	VDD Current	-	270	330	[mA]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)
P <sub>VDD</sub>	VDD Power	-	0.9	1.2	[Watt]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)

Note 1: Measurement condition:



VDD rising time

64 Gray pattern

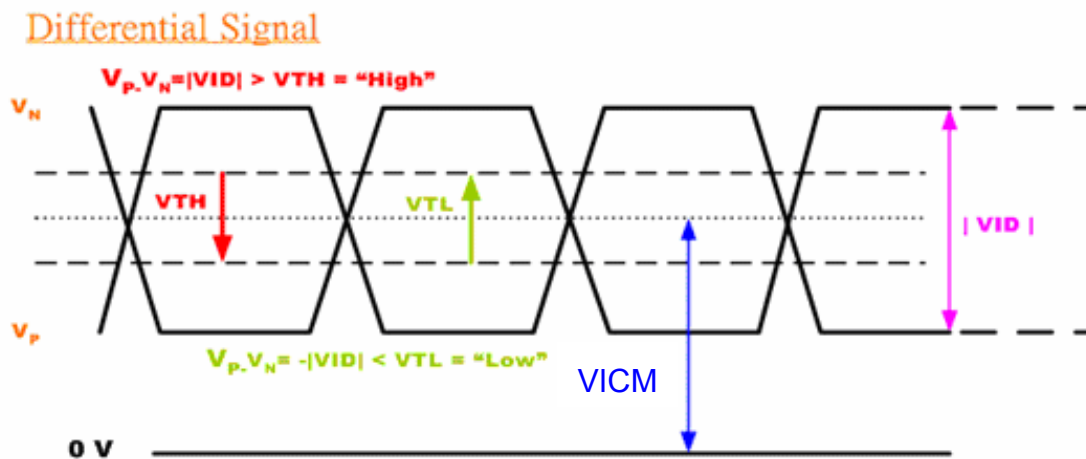


### 5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off.

Symbol	Item	Min.	Typ.	Max.	Unit	Remark
V <sub>TH</sub>	Differential Input High Threshold	-	-	100	[mV]	V <sub>ICM</sub> =1.2V
V <sub>TL</sub>	Differential Input Low Threshold	-100	-	-	[mV]	V <sub>ICM</sub> =1.2V
VID	Input Differential Voltage	100	400	600	[mV]	
V <sub>ICM</sub>	Differential Input Common Mode Voltage	1.1		1.6	[V]	V <sub>TH</sub> /V <sub>TL</sub> =±100mV

Note: LVDS Signal Waveform.



## 5.2 Backlight Unit

### 5.2.1 Parameter guideline for LED backlight

Following characteristics are measured under a stable condition using an inverter at 25 °C (Room Temperature):

Symbol	Parameter	Min.	Typ.	Max.	Unit	Remark
VCC	Input Voltage	10.8	12	12.6	[Volt]	
I <sub>VCC</sub>	Input Current	-	0.32	-	[A]	100% PWM Duty
/P <sub>VCC</sub>	Power Consumption	-	3.84		[Watt]	100% PWM Duty
F <sub>PWM</sub>	Dimming Frequency	200	-	12K	[Hz]	
	Dimming Voltage	3.3	5		V	
	Dimming Duty Cycle	5	-	100	%	
I <sub>F</sub>	LED Forward Current	-	200		mA	Ta = 25°C
V <sub>F</sub>	LED Forward Voltage		17.4			I <sub>F</sub> = 200mA, Ta = 85°C
P <sub>LED</sub>	LED Power Consumption	-	3.48	-	Watt	I <sub>F</sub> = 200mA, Ta = 25°C (total power)
Operation Lifetime		50,000			Hrs	I <sub>F</sub> = 200mA, Ta = 25°C

Note 1: Ta means ambient temperature of TFT-LCD module.

Note 2: VCC, I<sub>VCC</sub>, P<sub>VCC</sub>, are defined for LED B/L.(100% duty of PWM dimming)

Note 3: I<sub>F</sub>, V<sub>F</sub> are defined for each channel of LED Light Bar.

Note 4: If LMTFA084BS11 module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

Note 5: Operating life means brightness goes down to 50% initial brightness. Minimum operating life time is estimated data.



## 6. Signal Characteristic

### 6.1 Pixel Format Image

Following figure shows the relationship between input signal and LCD pixel format.

	1			2															799			800		
1st Line	R	G	B	R	G	B	. . . . .												R	G	B	R	G	B
	.			.			.												.			.		
600th Line	R	G	B	R	G	B	. . . . .												R	G	B	R	G	B
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## 6.2 Signal Description

LVDS is a differential signal technology for LCD interface and high speed data transfer device. The connector pin definition is as below.

Pin No.	Symbol	Description
1	VDD	Power Supply, 3.3V (typical)
2	VDD	Power Supply, 3.3V (typical)
3	UD	Vertical Reverse Scan Control, When UD=Low or NC → Normal Mode. When UD=High → Vertical Reverse Scan. <small>Note</small>
4	LR	Horizontal Reverse Scan Control, When LR=Low or NC → Normal Mode. When LR=High → Horizontal Reverse Scan. <small>Note</small>
5	RxIN1-	LVDS differential data input Pair 0
6	RxIN1+	
7	GND	Ground
8	RxIN2-	LVDS differential data input Pair 1
9	RxIN2+	
10	GND	Ground
11	RxIN3-	LVDS differential data input Pair 2
12	RxIN3+	
13	GND	Ground
14	RxCLKIN-	LVDS differential Clock input Pair
15	RxCLKIN+	
16	GND	Ground
17	SEL 68	LVDS 6/8 bit select function control, Low or NC → 6 Bit Input Mode. High → 8 Bit Input Mode. <small>Note</small>
18	NC	NC
19	RxIN4-	LVDS differential data input Pair 3. Must be set to NC in 6 bit input mode.
20	RxIN4+	

Note : “Low” stands for 0V. “High” stands for 3.3V. “NC” stands for “No Connected.”



## 6.3 Scanning Direction

The following figures show the image seen from the front view. The arrow indicates the direction of scan.



Fig. 1



Fig. 2



Fig. 3

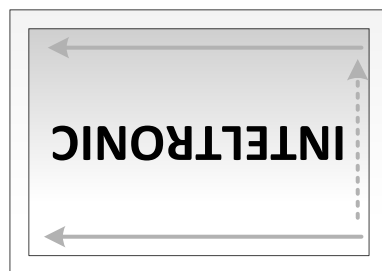


Fig. 4

Fig. 1 Normal scan (Pin3, UD = Low or NC ; Pin4, RL = Low or NC)

Fig. 2 Reverse scan (Pin3, UD = Low or NC ; Pin4, RL = High)

Fig. 3 Reverse scan (Pin3, UD = High ; Pin4, RL = Low or NC)

Fig. 4 Reverse scan (Pin3, UD = High ; Pin4, RL = High)

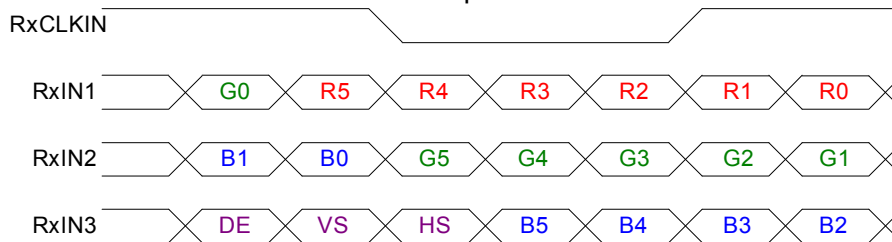




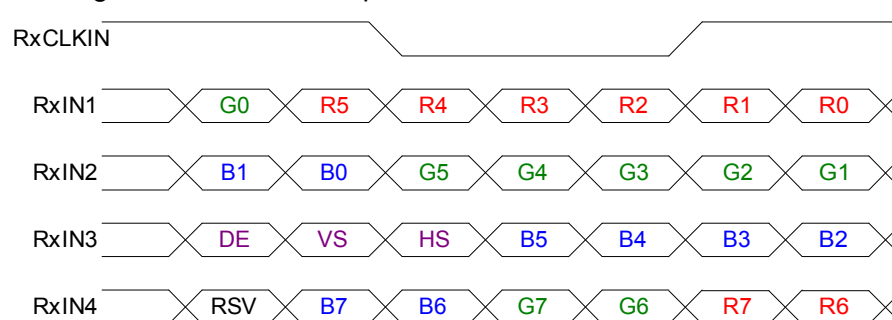
## 6.4 The Input Data Format

### 6.4.1 SEL68

SEL68 = "Low" or "NC" for 6 bits LVDS Input



SEL68 = "High" for 8 bits LVDS Input



Note1: Please follow PSWG.

Note2: R/G/B data 7:MSB, R/G/B data 0:LSB

Signal Name	Description	Remark
R7 R6 R5 R4 R3 R2 R1 R0	Red Data 7 (MSB) Red Data 6 Red Data 5 Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB)	Red-pixel Data Each red pixel's brightness data consists of these 8 bits pixel data.
G7 G6 G5 G4 G3 G2 G1 G0	Green Data 7 (MSB) GreenData 6 GreenData 5 GreenData 4 GreenData 3 GreenData 2 GreenData 1 GreenData 0 (LSB)	Green-pixel Data Each green pixel's brightness data consists of these 8 bits pixel data.
B7 B6 B5 B4 B3 B2 B1 B0	Blue Data 7 (MSB) Blue Data 6 Blue Data 5 Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 8 bits pixel data.
RxCLKIN+ RxCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

Note: Output signals from any system shall be low or Hi-Z state when VDD is off.



## 6.5 Interface Timing

### 6.5.1 Timing Characteristics

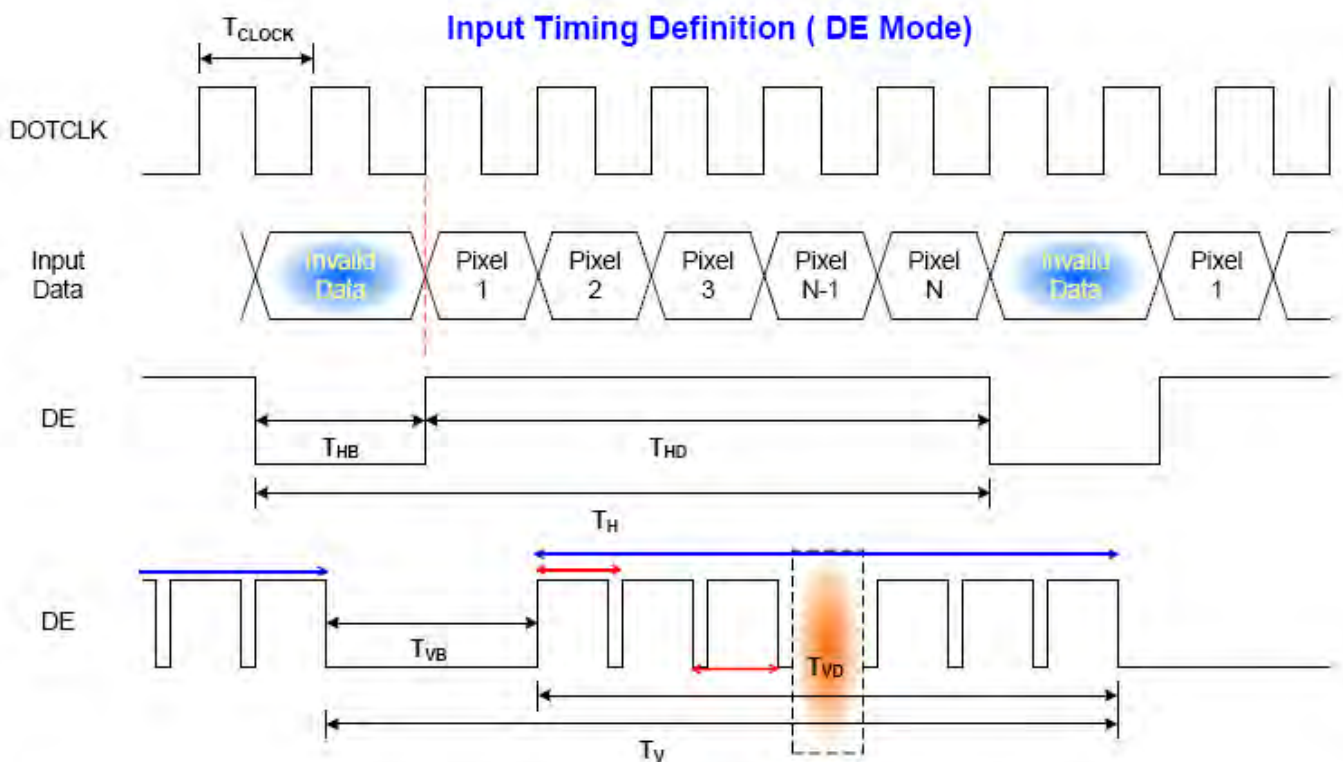
DE mode only

Parameter		Symbol	Min.	Typ.	Max.	Unit	Condition
Clock frequency		$1/T_{\text{Clock}}$	33.6	39.8	48.3	MHz	
Vertical Section	Period	$T_V$	608	628	650	$T_H$	
	Active	$T_{VD}$	600	600	600		
	Blanking	$T_{VB}$	8	28	50		
Horizontal Section	Period	$T_H$	920	1056	1240	$T_{\text{Clock}}$	
	Active	$T_{HD}$	800	800	800		
	Blanking	$T_{HB}$	120	256	440		

Note: Frame rate is 60 Hz.

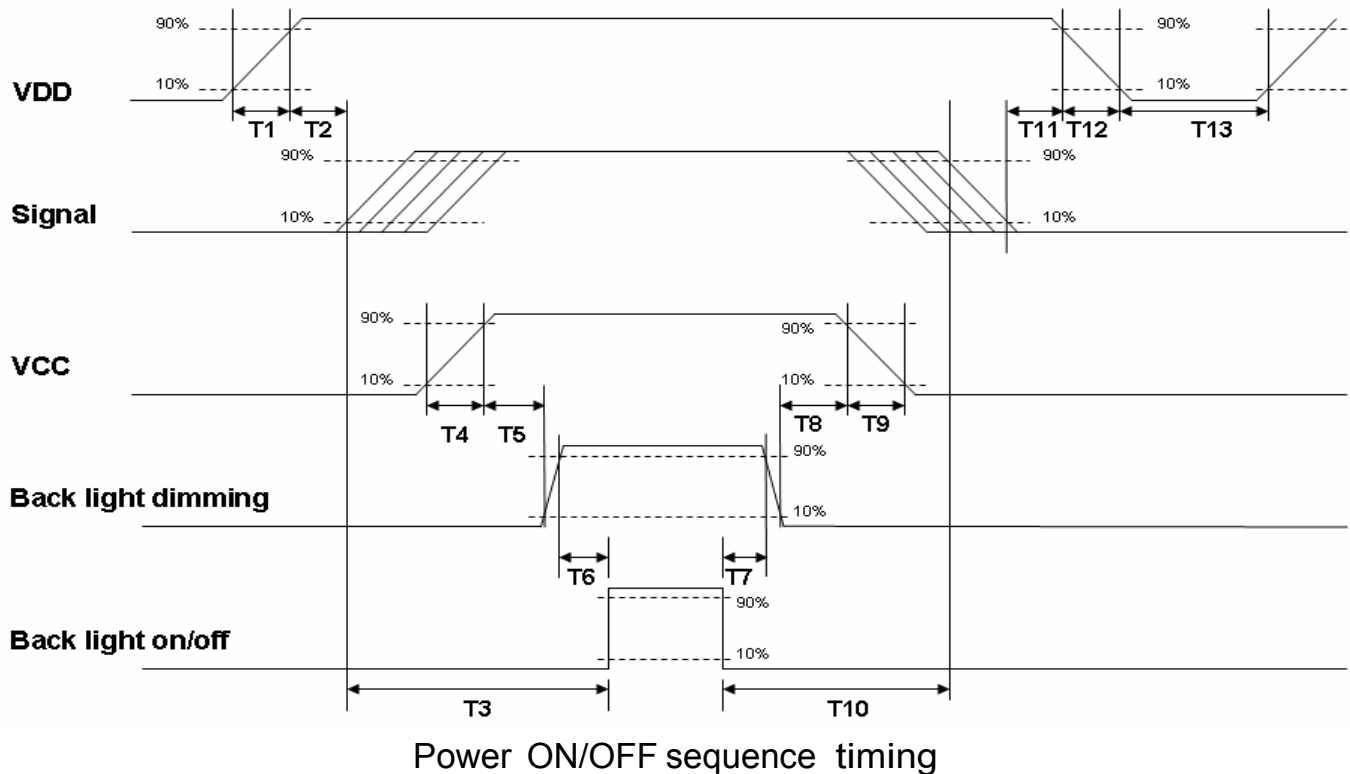
Note: DE mode.

### 6.5.2 Input Timing Diagram



## 6.6 Power ON/OFF Sequence

VDD power and BackLight on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Parameter	Value			Units
	Min.	Typ.	Max.	
T1	0.5	--	10	[ms]
T2	30	40	50	[ms]
T3	200	--	--	[ms]
T4	0.5	--	10	[ms]
T5	10	--	--	[ms]
T6	10	--	--	[ms]
T7	0	--	--	[ms]
T8	10	--	--	[ms]
T9	--	--	10	[ms]
T10	110	--	--	[ms]
T11	0	16	50	[ms]
T12	--	--	10	[ms]
T13	1000	--	--	[ms]

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.



## 7. Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

### 7.1 TFT LCD Signal (CN1): LVDS Connector

Connector Name / Designation	Signal Connector
Manufacturer	STM, Hirose or compatible
Connector Model Number	STM -MSB24013P20HA, Hirose- DF19LA-20P-1H or compatible
Mating Model Number	STM-P24013P20, Hirose-DF19-20S-1C or compatible

Pin No.	Signal Name	Pin No.	Signal Name
1	VDD	2	VDD
3	UD	4	LR
5	RxIN1-	6	RxIN1+
7	GND	8	RxIN2-
9	RxIN2+	10	GND
11	RxIN3-	12	RxIN3+
13	GND	14	RxCKIN-
15	RxCKIN+	16	GND
17	SEL 68	18	NC
19	RxIN4-	20	RxIN4+

### 7.2 LED Backlight Unit (CN2): LED Driver Connector

Connector Name / Designation	LED Connector
Manufacturer	JST
Connector Model Number	S7B-PH-SM4-TB
Mating Model Number	

Pin #	Symbol	Pin Description
1	VCC	12V input
2	VCC	12V input
3	VCC	12V input
4	GND	GND
5	Dimming	PWM
6	GND	GND
7	Display_ON/OFF	+5.0V:ON, 0V:OFF



### 7.3 LED Light Bar Input Connector (CN3):

Manufacturer	
Connector Model Number	A20D/HD2-2P
Mating Connector Model Number	S2B-PH-SM4-TB or compatible

Pin #	Symbol	Pin Description
1	AN1	LED anode
2	CA1	LED cathode

Pin #	Symbol	Cable color
1	AN1	Red
2	CA1	Black



## 8. Reliability Test Criteria

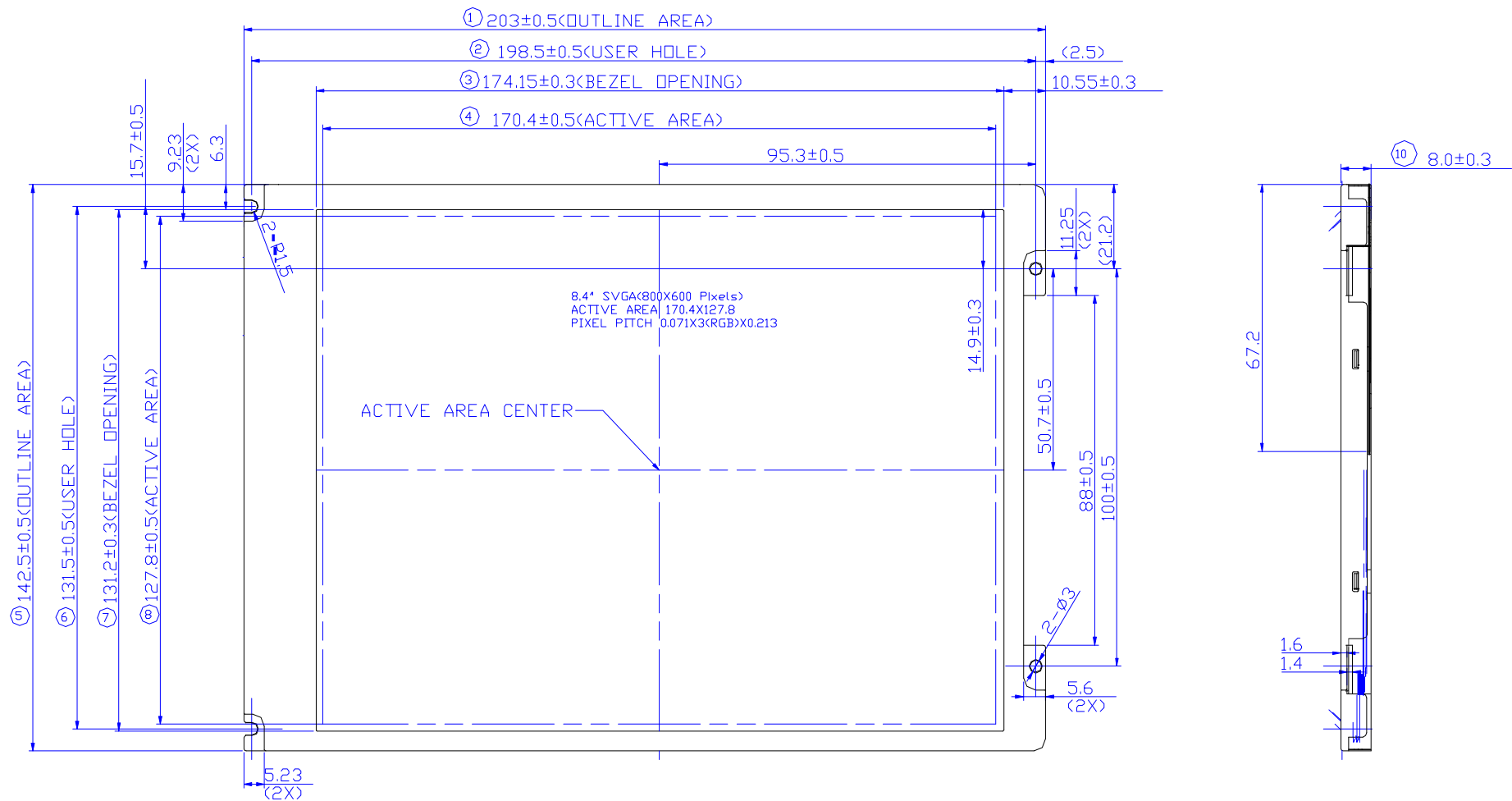
Items	Required Condition	Note
Temperature Humidity Bias	40 <sup>0</sup> C/90%,300 hours	
High Temperature Operation	85 <sup>0</sup> C,300 hours	
Low Temperature Operation	-30 <sup>0</sup> C,300 hours	
Hot Storage	85 <sup>0</sup> C,300 hours	
Cold Storage	-30 <sup>0</sup> C,300 hours	
Thermal Shock Test	-20 <sup>0</sup> C/30 min ,600C/30 min ,100cycles	
Shock Test (Non-Operating)	50G,20ms,Half-sine wave,( ±X, ±Y, ±Z)	
Vibration Test (Non-Operating)	1.5G, (10~200Hz, P-P) 30 mins/axis (X, Y, Z)	
On/off test	On/10 sec, Off/10 sec, 30,000 cycles	
ESD	Contact Discharge: ± 8KV, 150pF(330Ω ) 1sec, 8 points, 25 times/ point Air Discharge: ± 15KV, 150pF(330Ω ) 1sec, 8 points, 25 times/ point	Note 1

Note1: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost  
. Self-recoverable. No hardware failures.

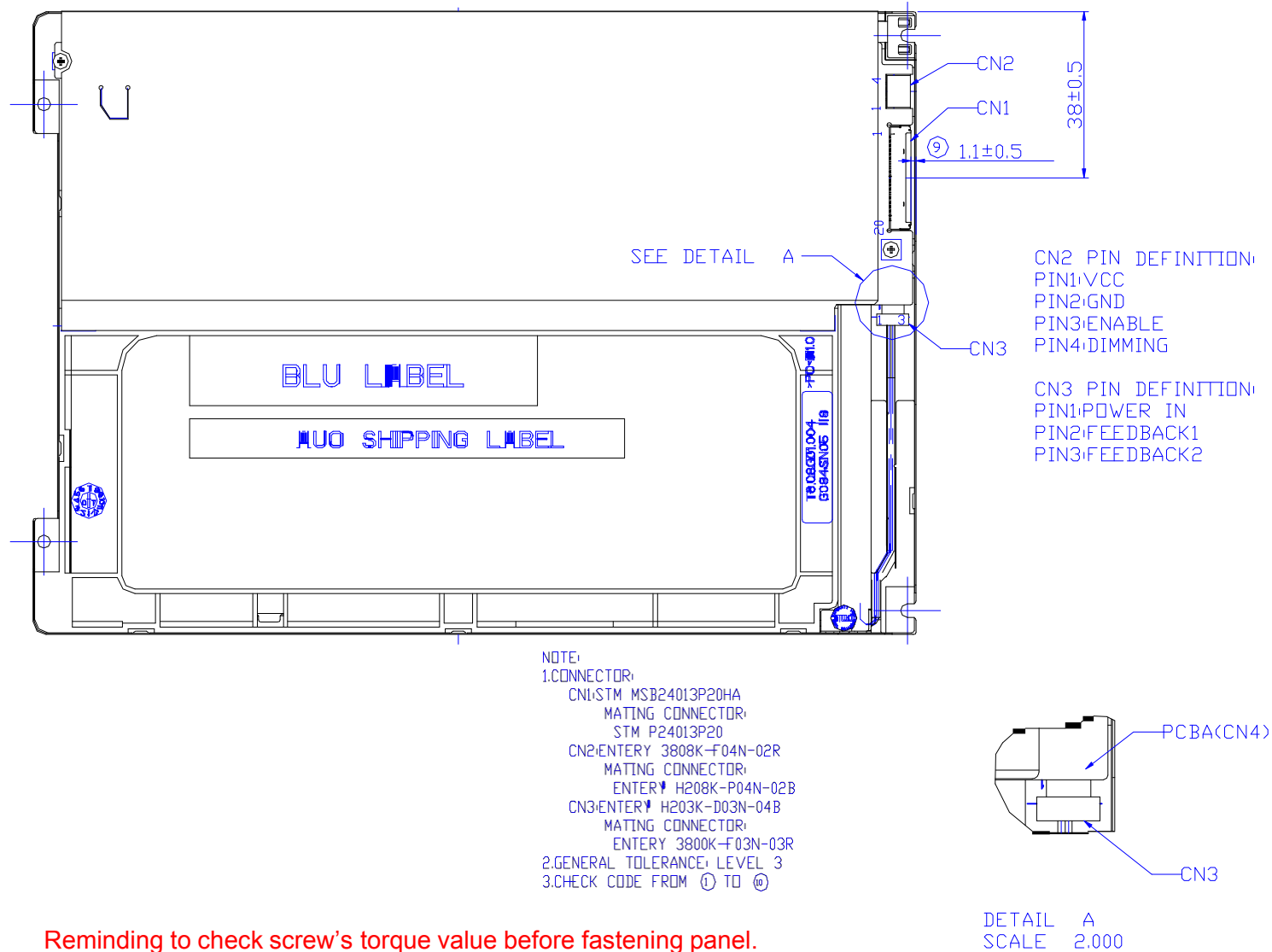


## 9. Mechanical Characteristics

### 9.1 LCM Front View



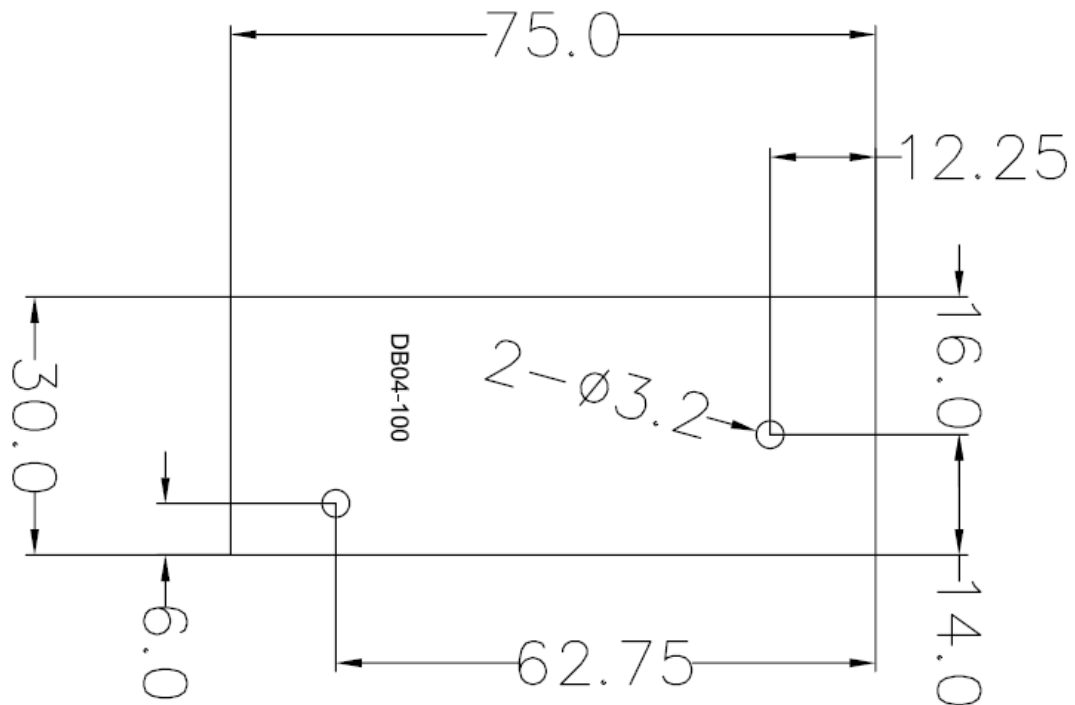
## 9.2 LCM Rear View





### 9.3 LED Driver board Outline Dimension

**Dimension : 75(L)\*30(W)\*8.65(H)mm**



## 10 .Inspection Specifications

The buyer (customer) shall inspect the modules within twenty calendar days since the delivery date (the "inspection period") at its own cost. The results of the inspection (acceptance or rejection) shall be recorded in writing, and a copy of this writing will be promptly sent to the seller.

The buyer may, under commercially reasonable reject procedures, reject an entire lot in the delivery involved if, within the inspection period, such samples of modules within such lot show an unacceptable number of defects in accordance with this incoming inspection standards, provided however that the buyer must notify the seller in writing of any such rejection promptly, and not later than within three business days of the end of the inspection period.

Should the buyer fail to notify the seller within the inspection period, the buyer's right to reject the modules shall be lapsed and the modules shall be deemed to have been accepted by the buyer.

## 11. Warranty

Inteltronic Inc. warrants to you, the original purchaser, that each of its products will be free from defects in materials and workmanship for one year from the date of purchase.

Inteltronic Inc. will be limited to replace or repair any of its module which is found and confirmed defective electrically or visually when inspected in accordance with Inteltronic Inc. general module inspection standard.

This warranty does not apply to any products which have been on customer's production line, repaired or altered by persons other than repair personnel authorized by Inteltronic Inc., or which have been subject to misuse, abuse, accident or improper installation. Inteltronic Inc. assumes no liability under the terms of this warranty as a consequence of such events.

If an Inteltronic Inc. product is defective, it will be repaired or replaced at no charge during the warranty period. For out-of-warranty repairs, you will be billed according to the cost of replacement materials, service time and freight. In returning the modules, they must be properly packaged with original package; there should be detailed description of the failures or defect.

## 12. RMA

Products purchased through Inteltronic Inc. and under warranty may be returned for replacement. Contact [support@inteltronicinc.com](mailto:support@inteltronicinc.com) for RMA number and procedures



# Office Locations



Inteltronic Inc.  
[www.inteltronicinc.com](http://www.inteltronicinc.com)  
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