



**12.1 inch TFT LCD
without Touch Panel
SPECIFICATION**

MODEL NAME: LMTAA121XX11

Date: 2013/ 07 / 05

Customer Signature		
Customer		
Approved Date	Approved By	Reviewed By

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RECORD OF REVISION

Version and Date	Page	Old description	New description	Remark
0.1 2013/07/05	All	First Edition for customer		



1. HANDLING PRECAUTIONS

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open or modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 10) After installation of the TFT Module into an enclosure, do not twist nor bend the TFT Module even momentarily. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.



2. General Description

2.1, Overview

This specification applies to the Color Active Matrix Liquid Crystal Display composed of a TFT-LCD display a LED backlight system. The screen format is intended to support WXGA (1280(H) x 800(V)) MVA mode screen and 262k / 16.2M colors.

LED driving board for backlight unit is not included.

2.2 Features

- Sunlight readable display, 1000nits by LED backlight.
- Wide viewing angle
- Low power consumption
- Wide operation temperature
- RoHS Compliance

2.3 Application

Industrial Application; especial kiosk and digital signage display.



2.4 Display Specifications

Items	Unit	Specification
Screen Diagonal	inch	12.1
Active Area	mm	261.12 (H) x 163.2 (V)
Pixels H x V	pixels	1280 x3(RGB) x 800
Pixels Pitch	um	204 (per one triad) x 204
Pixel Arrangement		RGB Vertical stripe
Display mode		MVA mode, normally black
White luminance (center)	Cd/m ²	1000 (Typ.)
Contrast ratio		700 (Typ.)
Optical Response Time	msec	23 ms (Typ. on/off)
Normal Input Voltage Vcc	Volt	3.3
Power Consumption (Vcc Line + LED backlight)	Watt	21.68 (Vcc line=2.68; LED line=19W)
Weight	Grams	455 max.
Physical size	mm	278(H) x 184(V) x 8.16(D) (Max.)
Electrical Interface		One Chanel LVDS
Support Colors		262K / 16.2 M colors
Surface Treatment		Anti-Glare, 3H
Temperature range		
Operating	°C	-10 ~ 70
Storage (Shipping)	°C	-20 ~ 80
RoHS Compliance		RoHS Compliance



2.5 Optical Characteristics

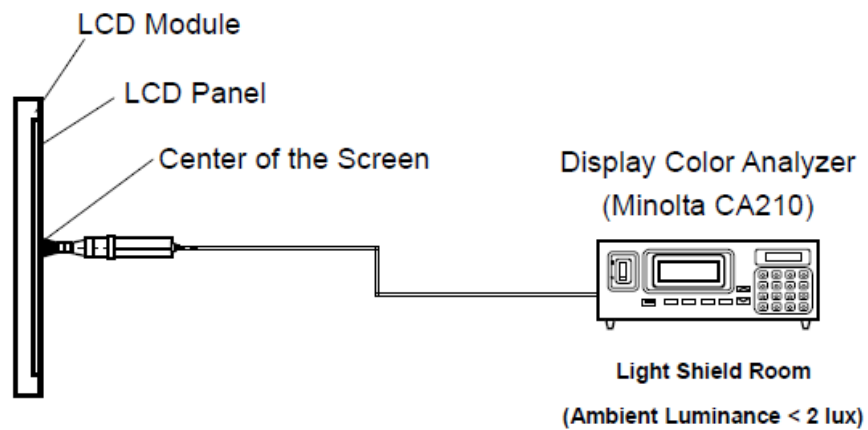
The following optical characteristics are measured under stable condition at 25 °C

Items	Unit	Conditions	Min.	Typ.	Max.	Note
Viewing angle	Deg.	Horizontal (Right) CR=10 (Left)	160	176		2
		Vertical (Up) CR=10 (Down)	160	176		
Contrast Ratio		Normal Direction	800	1000		3
Response Time	msec	Raising time (T_{rR})		15		4
		Falling time (T_{rF})		10		
		Raising + Falling		25		
Color / Chromaticity Coordinates (CIE)		Red x	-0.03	0.565	+0.03	5
		Red y		0.351		
		Green x		0.357		
		Green y		0.590		
		Blue x		0.155		
		Blue y		0.131		
Color coordinates (CIE) White		White x		0.310		
		White y		0.330		
Center Luminance	Cd/m ²		800	1000		6
Luminance Uniformity	%		70	75		7
Crosstalk (in 60 Hz)	%				1.5	
Flicker	dB				-20	

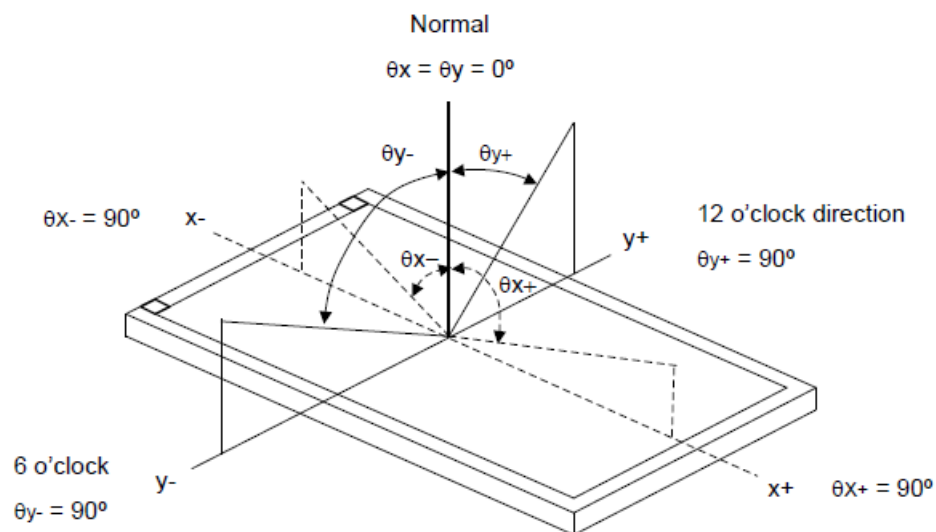


Note 1: Measurement method

The LCD module should be stabilized at given temperature for 0.5 hour to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 1 hour in a windless room.



Note 2: Definition of viewing angle

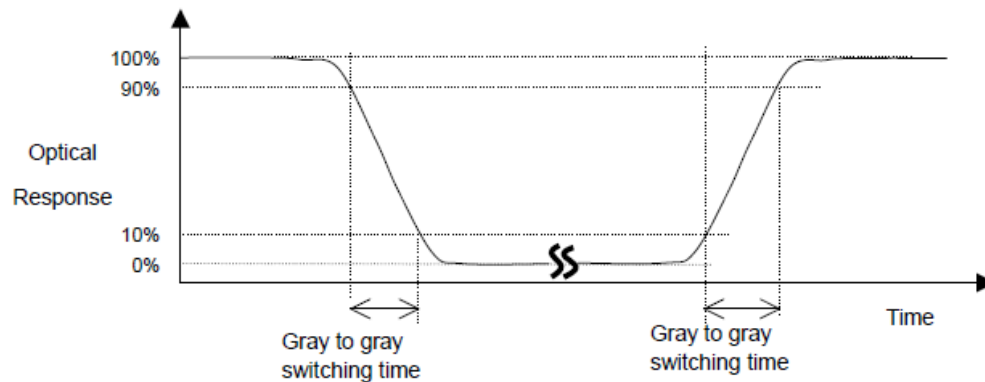


Note 3: Contrast ratio is measured by Minolta CA210



Note 4: Definition of Response time

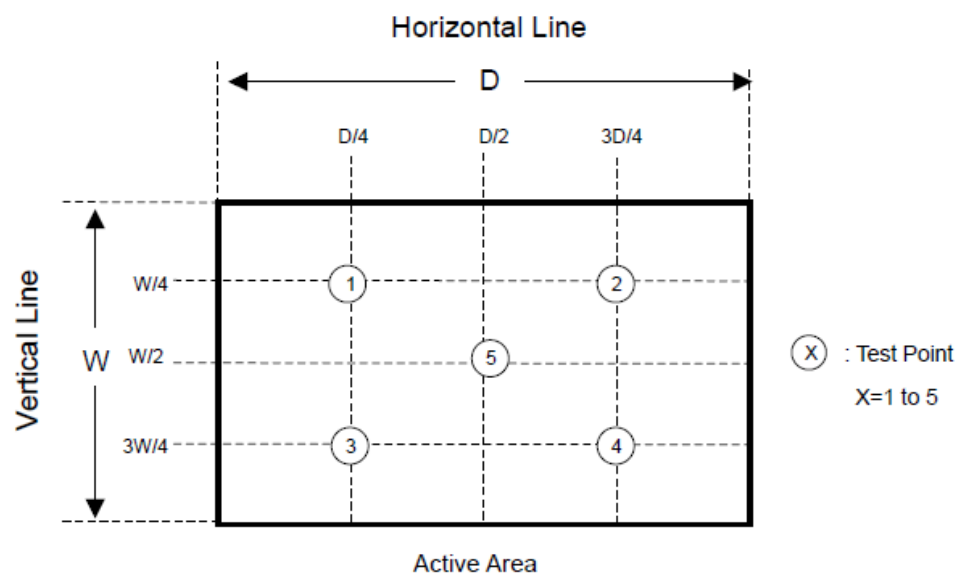
The output signals of photo detector are measured when the input signals are changed from “Full Black” to “Full White” (rising time), and from “Full White” to “Full Black” (falling time), respectively. The response time is interval between the 10% and 90% of amplitudes. Please refer to the figure as below.



Note 5: Color chromaticity and coordinates (CIE) is measured by Minolta CA210

Note 6: Center luminance is measured by Minolta CA210

Note 7: Luminance uniformity of these 5 points is defined as below and measured by Minolta CA210

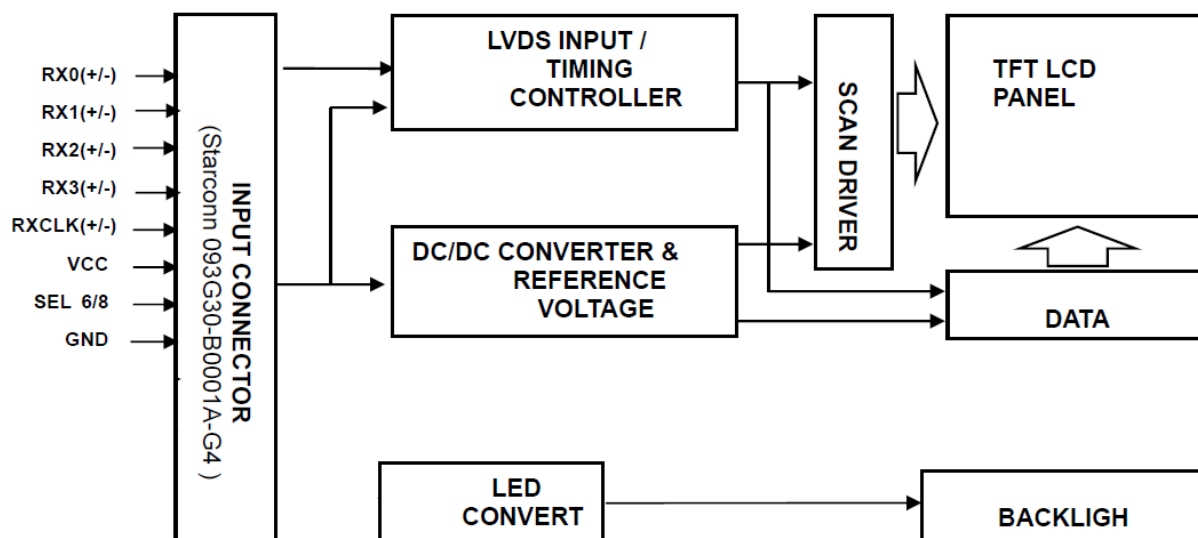


$$\text{Uniformity} = (\text{Min. Luminance of 5 points}) / (\text{Max. Luminance of 5 points})$$



3. Functional Block Diagram

The following diagram shows the functional block of the 12.1 inches Color TFT-LCD Module:



4. Absolute Maximum Ratings

Absolute maximum ratings of the module are as following:

4.1 TFT LCD Module

Items	Symbol	Min	Max	Unit	Conditions
Power supply voltage	V _{CCS}	-0.3	4.0	Volt	
Logic input voltage	V _{IN}	-0.3	V _{CCS} +0.3	Volt	

4.2 Backlight unit

Items	Symbol	Min	Max	Unit	Conditions
LED Current	I _{LED}	--	600	mA	Note 1, 2

4.3 Absolute Ratings of Environment

Items	Symbol	Values			Unit	Conditions
		Min.	Typ.	Max.		
Operation temperature	T _{OP}	-10	-	70	°C	Note 3
Operation Humidity	H _{OP}	5		90	%	
Storage temperature	T _{ST}	-20		80	°C	
Storage Humidity	H _{ST}	5		90	%	

Note 1: With in Ta= 25°C

Note 2: Permanent damage to the device may occur if exceed maximum values

Note 3: For quality performance, please refer to IIS (Incoming Inspection Standard).



5. Electrical characteristics

5.1 TFT LCD Module

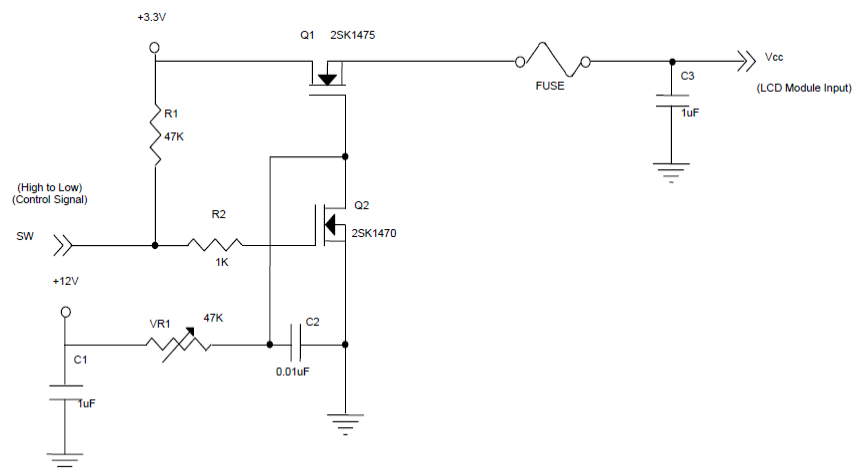
5.1.1 Power Specification

Input power specifications are as follows

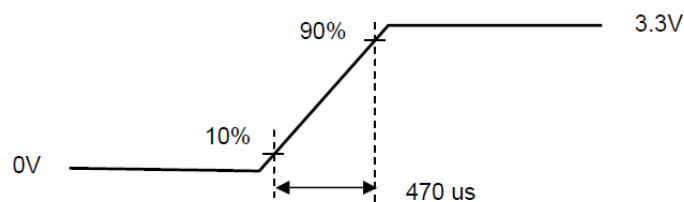
Parameter		Symbol	Value			Unit	Note
			Min.	Typ	Max.		
Power Supply Voltage		V _{CC}	3.0	3.3	3.6	V	-
Permissible Ripple Voltage		V _{RP}	-	50	-	mV	-
Rush Current		I _{RUSH}	-	-	1.5	A	(2)
Initial Stage Current		I _{IS}	-	-	1.0	A	(2)
Power Supply Current	White	-	450	500	550	mA	(3)a
	Black	-	350	385	420	mA	(3)b
LVDS Differential Input High Threshold		V _{TH(LVDS)}	-	-	+100	mV	(5), V _{CM} =1.2V
LVDS Differential Input Low Threshold		V _{TL(LVDS)}	-100	-	-	mV	(5) V _{CM} =1.2V
LVDS Common Mode Voltage		V _{CM}	1.125	-	1.375	V	(5)
LVDS Differential Input Voltage		V _{ID}	100	-	600	mV	(5)
Terminating Resistor		R _T	-	100	-	Ohm	
Power per EBL WG		P _{EBL}	-	2.68	-	W	(4)

Note (1) The assembly should be always operated within above ranges.

Note (2) Measurement Conditions:



V_{CC} rising time



Note (3) The specified power supply current is under the conditions at V_{CC} = 3.3V, T_a = 25 ± 2 °C, f_v = 60 Hz, whereas a power dissipation check pattern below is displayed.



a. White Pattern



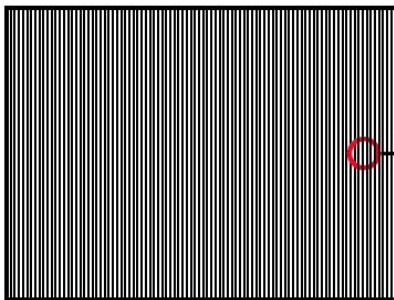
Active Area

b. Black Pattern

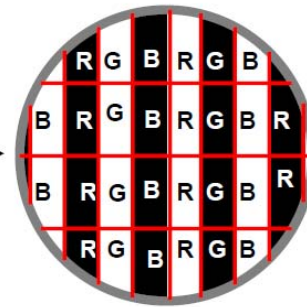


Active Area

c. Vertical Stripe Pattern



Active Area



5.1.2 Interface connection

Pin assignment

Pin No	Symbol	Description	Note
1	NC	No Connection	-
2	NC	No Connection	-
3	NC	No Connection	-
4	NC	No Connection	-
5	NC	No Connection	-
6	NC	No Connection	-
7	GND	Ground	-
8	GND	Ground	-
9	VCC	Power supply: +3.3V	
10	VCC	Power supply: +3.3V	-
11	GND	Ground	-
12	GND	Ground	-
13	RX0-	Negative transmission data of pixel 0	-
14	RX0+	Positive transmission data of pixel 0	-
15	GND	Ground	-
16	RX1-	Negative transmission data of pixel 1	-
17	RX1+	Positive transmission data of pixel 1	-
18	GND	Ground	-
19	RX2-	Negative transmission data of pixel 2	-
20	RX2+	Positive transmission data of pixel 2	-
21	GND	Ground	-
22	RXCLK-	Negative of clock	-
23	RXCLK+	Positive of clock	-
24	GND	Ground	-
25	RX3-	Negative transmission data of pixel 3	-
26	RX3+	Positive transmission data of pixel 3	-
27	GND	Ground	-
28	SEL6/8	LVDS 6/8 bit select function control,	-2
		Low or NC → 6 bit Input Mode	
		High → 8bit Input Mode	
29	GND	Ground	-
30	GND	Ground	-

Note (1) Connector Part No.: Starconn 093G30-B0001A-G4

Note (2) “Low” stands for 0V. “High” stands for 3.3V



5.2 Backlight Unit

Parameter guideline is under stable conditions at 25°C (Room Temperature):

Parameter	Min	Typ	Max	Unit	Note
LED voltage (VL)		29.7		[V]	2
LED current (IL)		(320)		[mA]	2,
LED Power (PL)		(9.5)		[W]	
LED Life Time(LTLED)		40,000		[Hour]	1

Note 1: The “LED lift time” is defined as the module brightness decrease to 50% original brightness that the ambient temperature is 25°C and typical LED Current at TBD mA (Long lifetime mode).

LED Light bar connector

Recommended connector : JOIN TEK JT1025-1021

Pin no	Symbol	I/O	Description	Remark
1	VLED+	P	Backlight LED anode	
2	VLED-	P	Backlight LED cathode	



5.3 Color Data Input Assignment

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage



5.4 TFT-LCD Interface Timing

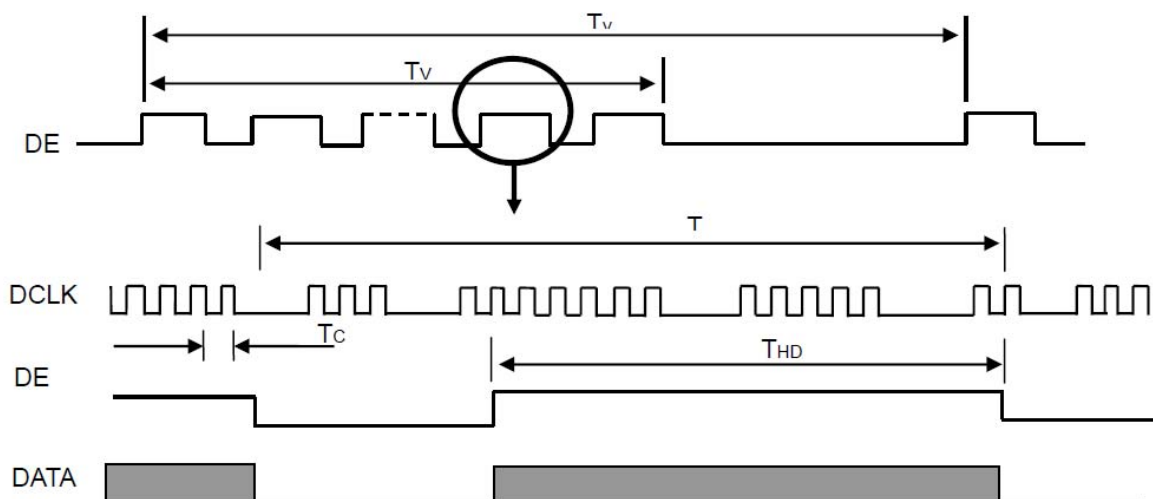
5.5.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max	Unit	Not e
DCLK	Frequency	1/Tc	67.45	71	74.55	MHz	-
DE	Vertical Total Time	TV	810	823	1000	TH	-
	Vertical Addressing Time	TVD	800	800	800	TH	-
	Horizontal Total Time	TH	1360	1440	1600	Tc	-
	Horizontal Addressing Time	THD	1280	1280	1280	Tc	-

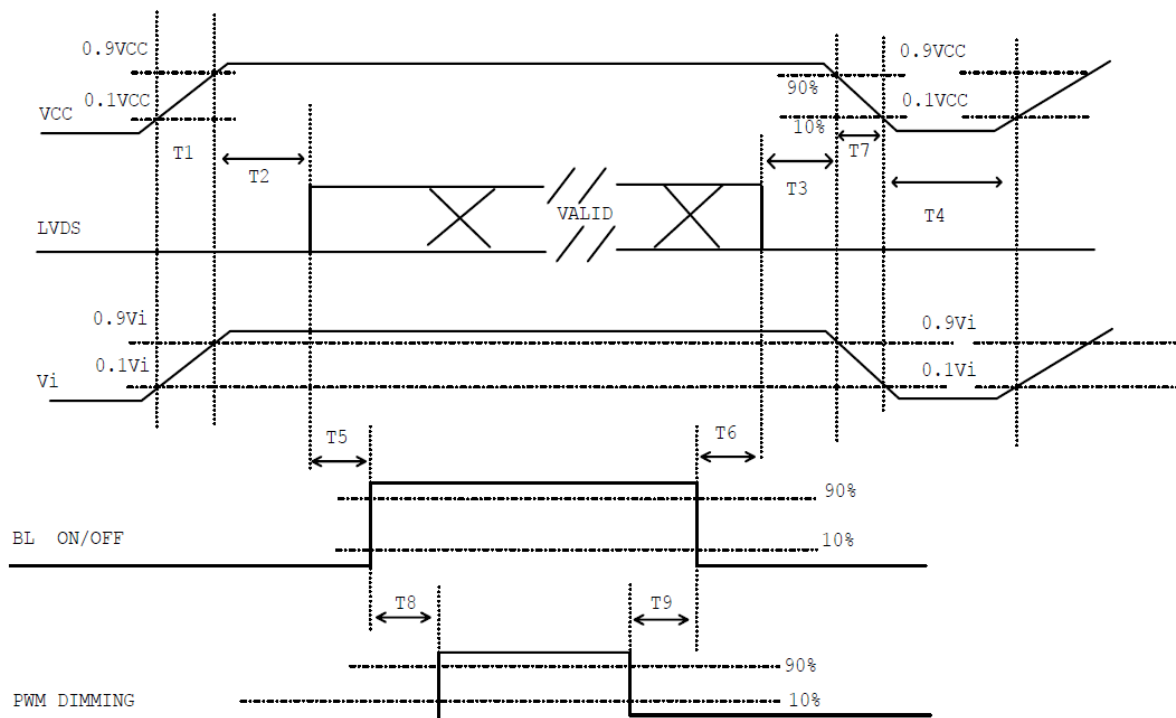
Note (1) Since this assembly is operated in DE only mode, Hsync and Vsync input signals should be set to low logic level. Otherwise, this assembly would operate abnormally.

INPUT SIGNAL TIMING DIAGRAM



5.5 Power ON/OFF Sequence

To prevent a latch-up or DC operation of LCD assembly, the power on/off sequence should be as the diagram below.



Note (1) Please avoid floating state of interface signal at invalid period.

Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD VCC to 0 V.

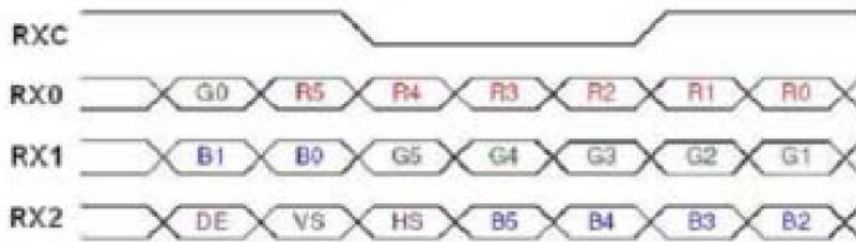
Note (3) The Backlight converter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight converter power must be turned off before the power supply for the logic and the interface signal is invalid.

Parameter	Value			Units
	Min	Typ	Max	
T1	0.5	---	10	ms
T2	0	---	50	ms
T3	0	---	50	ms
T4	500	---	---	ms
T5	200	---	---	ms
T6	20	---	---	ms
T7	5	---	300	ms
T8	10	---	---	ms
T9	10	---	---	ms

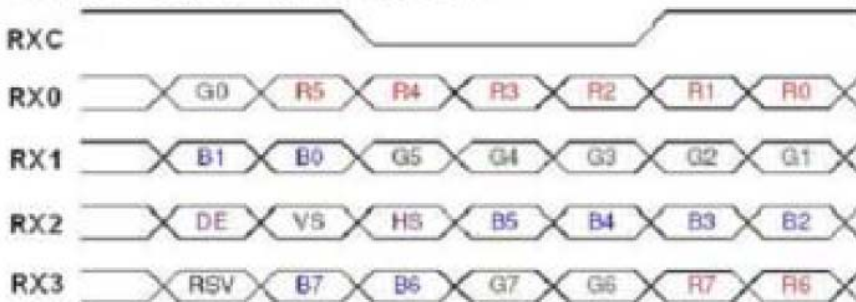


5.6 The Input Data Format

SEL 6/8="Low" or "NC" for 6 Bits LVDS



SEL 6/8="High" for 8 Bits LVDS



Note (1) R/G/B data 7: MSB, R/G/B data 0: LSB

Note (2) Please follow PSWG

Signal Name	Description	Remark
R7	Red Data 7 (MSB)	Red-pixel Data Each red pixel's brightness data consists of these 8 bits pixel data.
R6	Red Data 6	
R5	Red Data 5	
R4	Red Data 4	
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0 (LSB)	
G7	Green Data 7 (MSB)	Green-pixel Data Each green pixel's brightness data consists of these 8 bits pixel data.
G6	GreenData 6	
G5	GreenData 5	
G4	GreenData 4	
G3	GreenData 3	
G2	GreenData 2	
G1	GreenData 1	
G0	GreenData 0 (LSB)	
B7	Blue Data 7 (MSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 8 bits pixel data.
B6	Blue Data 6	
B5	Blue Data 5	
B4	Blue Data 4	
B3	Blue Data 3	
B2	Blue Data 2	
B1	Blue Data 1	
B0	Blue Data 0 (LSB)	
RXCLKIN+ RXCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	



6. Reliability Test

Environment test conditions are listed as following table.

Items	Required Condition	Note
Temperature Humidity Bias (THB)	Ta= 50℃, 90%RH, 240hours	
High Temperature Operation (HTO)	Ta= 70℃, 50%RH, 240hours	3
Low Temperature Operation (LTO)	Ta= -10℃, 240hours	
High Temperature Storage (HTS)	Ta= 80℃, 240hours	
Low Temperature Storage (LTS)	Ta= -20℃, 240hours	
Drop Test	Height: 60 cm, package test	
Thermal Shock Test (TST)	-20℃/30min, 80℃/30min, 100 cycles	
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (ElectroStatic Discharge)	Contact Discharge: \pm 8KV, 150pF(330 Ω) 1sec, 9 points, 25 times/ point.	
	Air Discharge: \pm 15KV, 150pF(330 Ω) 1sec 9 points, 25 times/ point.	

Note 1: The TFT-LCD module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20℃ to 60℃, and back again. Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 2: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost. Self-recoverable. No hardware failures.

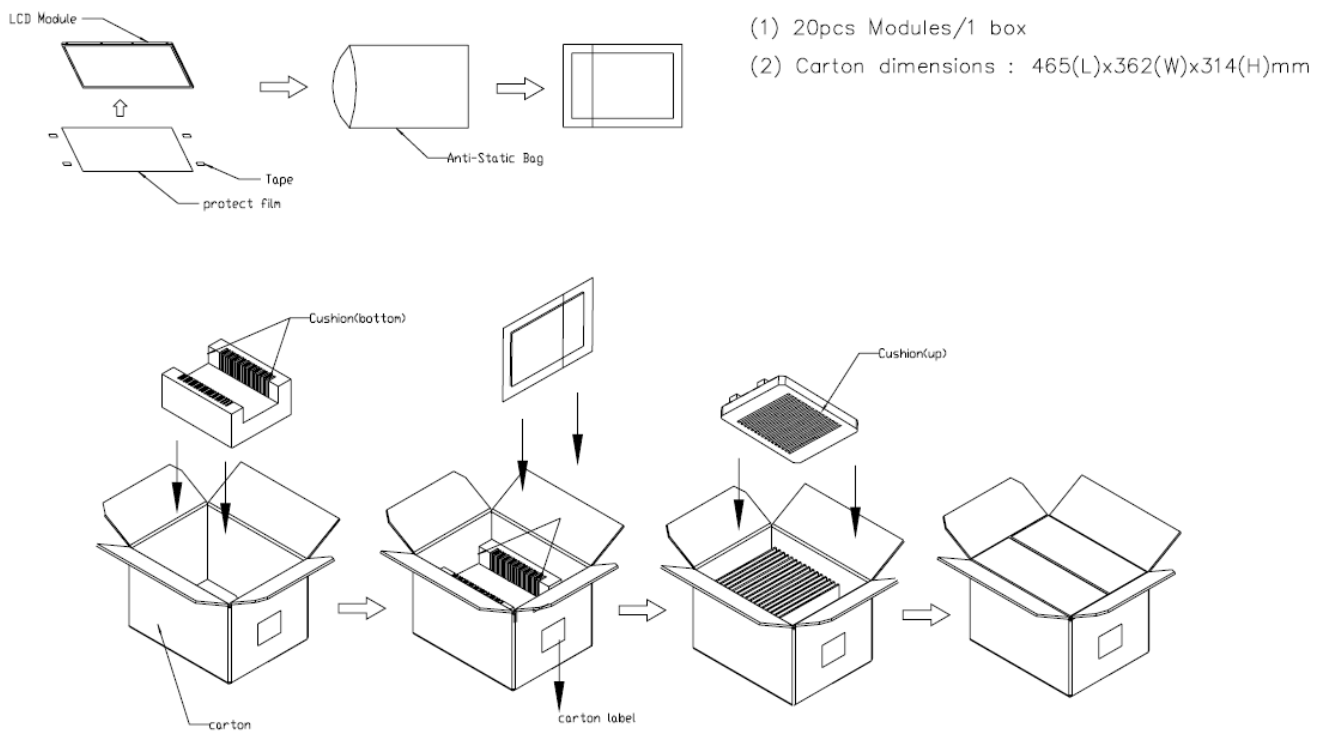
Note 3: The test items are tested by open frame type chassis.



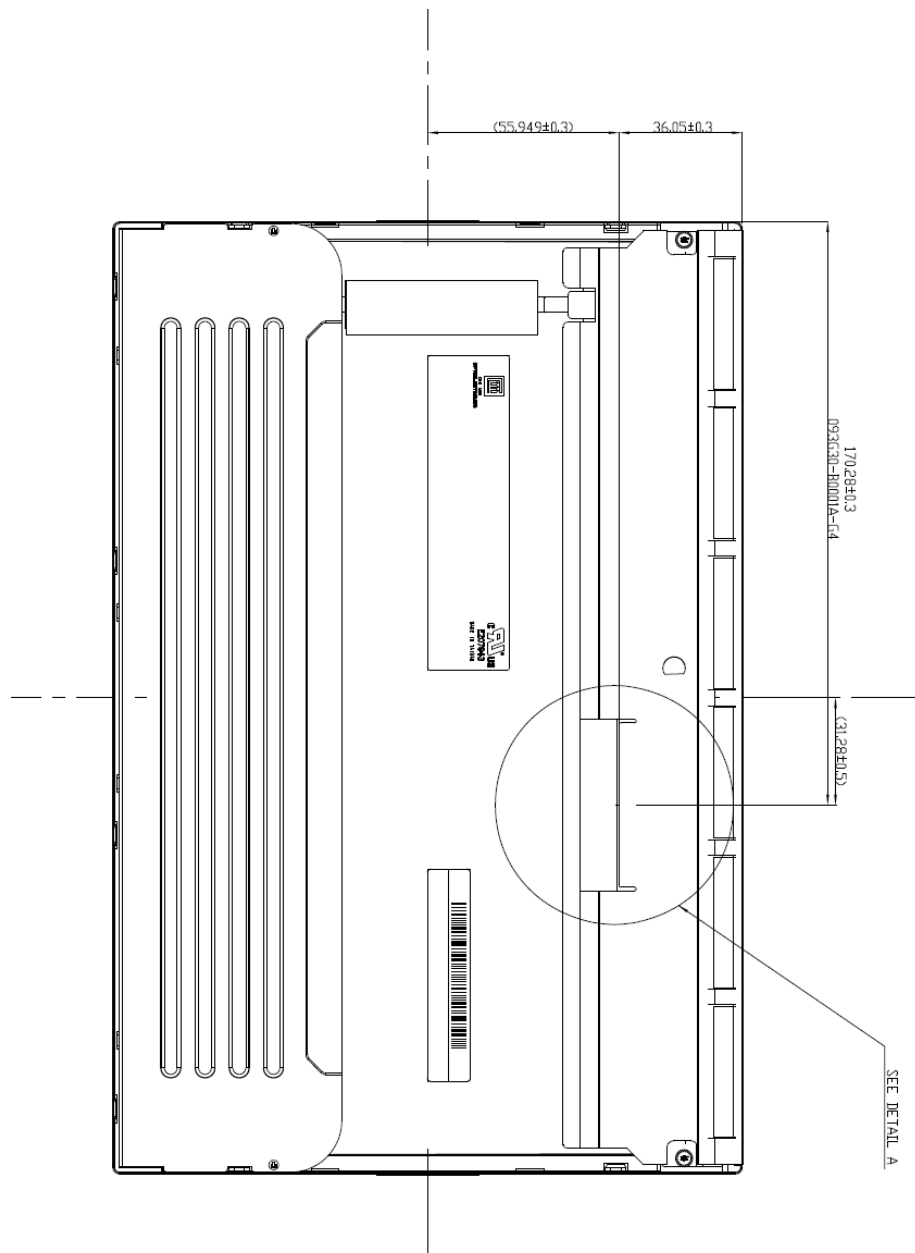
7. Shipping Label & Package (TBD)

(1) Carton Packing should have no failure in the following reliability test items.

Test Item	Test Conditions	Note
Vibration	ISTA STANDARD Random, Frequency Range: 2 – 200 Hz Top & Bottom: 30 minutes (+Z), 10 min (-Z), Right & Left: 10 minutes (X) Back & Forth 10 minutes (Y)	Non Operation
Dropping Test	1 Angle, 3 Edge, 6 Face, 61 cm	Non Operation

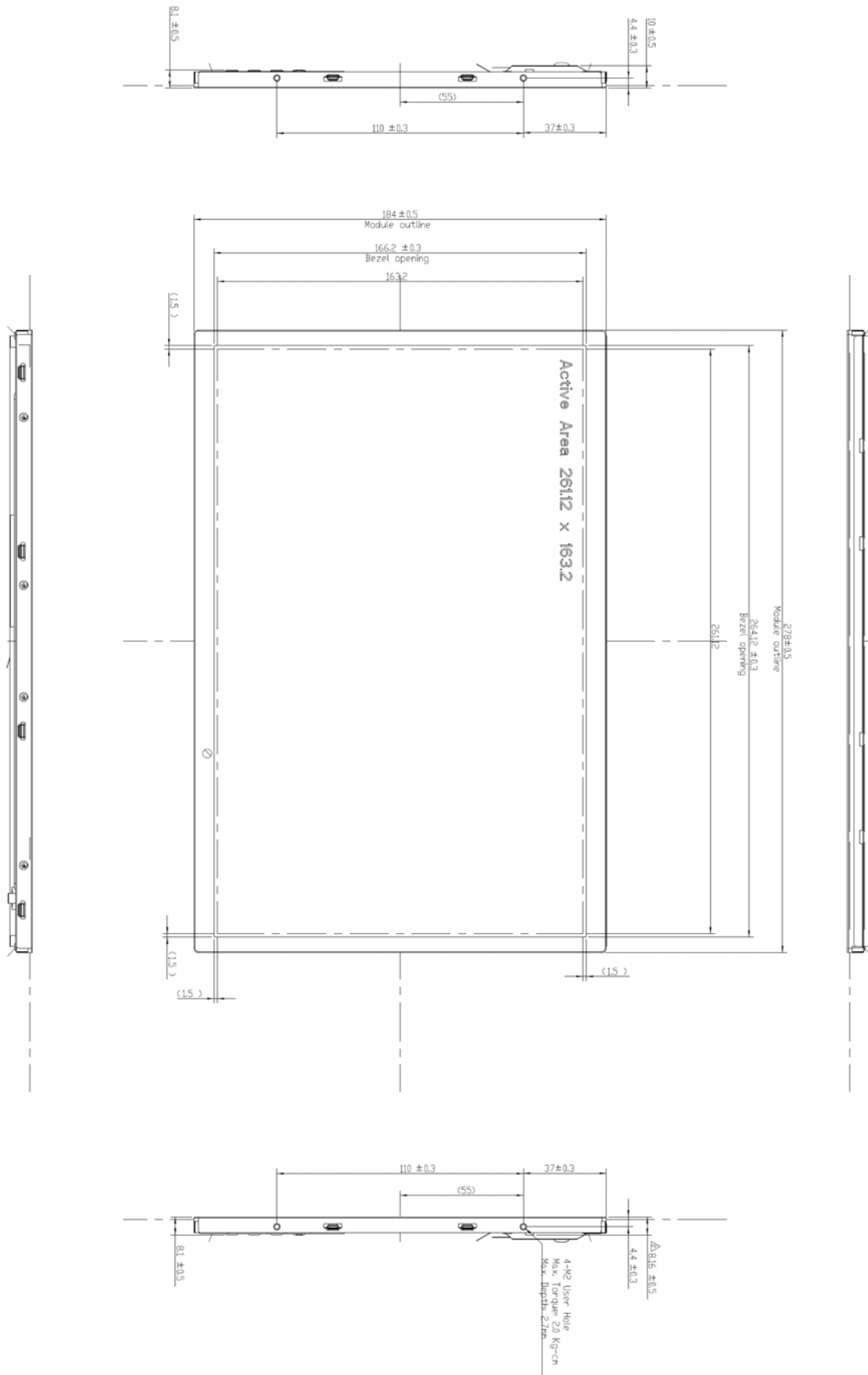


8. Mechanical Characteristic



(Note: The LED connectors are not showing)





9 .Inspection Specifications

The buyer (customer) shall inspect the modules within twenty calendar days since the delivery date (the "inspection period") at its own cost. The results of the inspection (acceptance or rejection) shall be recorded in writing, and a copy of this writing will be promptly sent to the seller.

The buyer may, under commercially reasonable reject procedures, reject an entire lot in the delivery involved if, within the inspection period, such samples of modules within such lot show an unacceptable number of defects in accordance with this incoming inspection standards, provided however that the buyer must notify the seller in writing of any such rejection promptly, and not later than within three business days of the end of the inspection period.

Should the buyer fail to notify the seller within the inspection period, the buyer's right to reject the modules shall be lapsed and the modules shall be deemed to have been accepted by the buyer.

10. Warranty

Inteltronic Inc. warrants to you, the original purchaser, that each of its products will be free from defects in materials and workmanship for one year from the date of purchase.

Inteltronic Inc. will be limited to replace or repair any of its module which is found and confirmed defective electrically or visually when inspected in accordance with Inteltronic Inc. general module inspection standard.

This warranty does not apply to any products which have been on customer's production line, repaired or altered by persons other than repair personnel authorized by Inteltronic Inc., or which have been subject to misuse, abuse, accident or improper installation. Inteltronic Inc. assumes no liability under the terms of this warranty as a consequence of such events.

If an Inteltronic Inc. product is defective, it will be repaired or replaced at no charge during the warranty period. For out-of-warranty repairs, you will be billed according to the cost of replacement materials, service time and freight. In returning the modules, they must be properly packaged with original package; there should be detailed description of the failures or defect.

11. RMA

Products purchased through Inteltronic Inc. and under warranty may be returned for replacement. Contact support@inteltronicinc.com for RMA number and procedures



Office Locations



Inteltronic Inc.
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Fax: 510-471-9901
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